# Piezoelectric RF SAW-based Energy Detectors

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#### Purpose

DARPA wants radio signal-powered sensors that draw no energy on standby. Traditional electronics are inefficient in this low power (2  $\mu$ V at 50  $\Omega$ ) regime.

Here, we build piezoelectric devices that



rectify an input RF wave into measurable DC current – an alternative paradigm worth exploration.

Fab Process

negative photoresist —

4" 128° Y-cut black lithium

niobate (LiNbO<sub>3</sub>) wafer

negative photoresist

75 nm Au on 10 nm Ti

dissolve resist in 1165

(1-methyl-2-pyrrolidone);

(nLOF 2020)

(evaporated)

metals lift off



#### Experiments

(1) This is one test chip, in schematic and picture views. InterDigitated Transducers (IDTs) on a piezoelectric substrate are built on either side of a piece of graphene. A surface acoustic wave (SAW) is generated from one IDT at a time via the inverse piezoelectric effect. A bias voltage is placed across the graphene and the circuit current measured. We expect the SAW to push electrons through the graphene, creating additional current – the acoustoelectric effect.
(2) A SAW frequency sweep finds points with minimum IDT insertion loss.

(3) Fixing SAW frequency at a peak (192.73 MHz), we sweep SAW amplitude.
(4) We repeat (3) with the opposite IDT. As the SAW travels in the opposite direction, the current change, too, is inverted, suggesting this change is due to the acoustoelectric effect.
(5) A different device was built with a curved IDT. The SAW created by this IDT, as measured by a scanning vibrometer,

### Conclusions & Future

This project demonstrates the acoustoelectric effect and focusing IDTs. Although the results presented here are not quite new<sup>[2]</sup>, confirmation is nice.

Next, we hope to optimize SAW power transfer efficiency and use its generated current to actuate a NEMS switch.

[2]: Nash et. al., APL, 2013, DOI:10.1063/1.4822121.

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(following 10 nm of Ti for adhesion) and pattern it using a previously applied liftoff mask for electrodes.

Next, a similar liftoff process<sup>[1]</sup> is used to pattern commercial CVD-grown monolayer graphene, which comes sandwiched between copper and PMMA. So far, minimum graphene feature size is approximately 100 μm.

[1]: Kim et. al., ACS-AMI, 2017, DOI:10.1021/acsami.7b05790.

focused, concentrating energy. This may prove useful later.

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